/ Descriptions

TO-3P NPN Silicon NPN transistor in a TO-3P Plastic Package.

/ Features

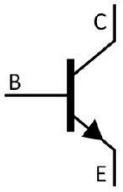
TIP36C

Low leakage current, high current gain bandwidth product, Complementary to TIP36C.

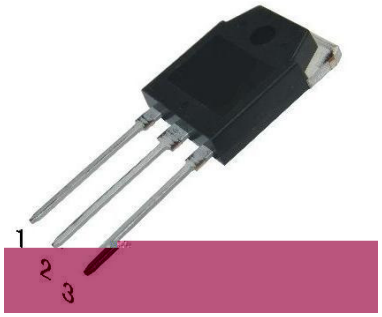
/ Applications

Power amplifier and switching applications.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

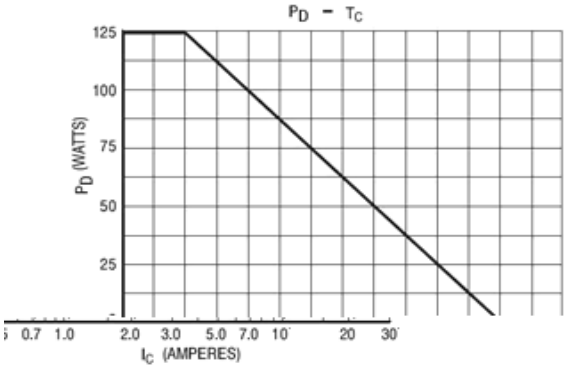
/ h_{FE} Classifications & Marking

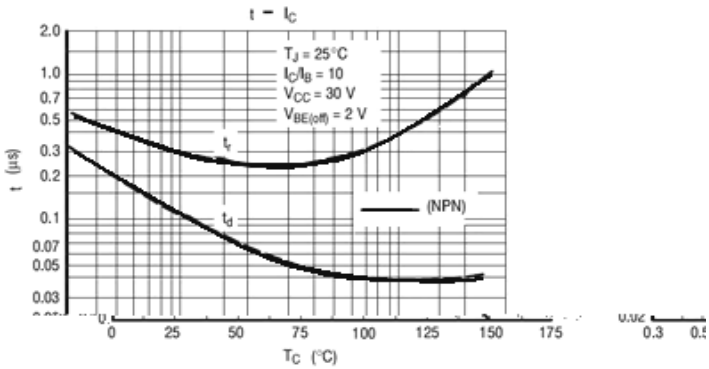
See Marking Instructions.

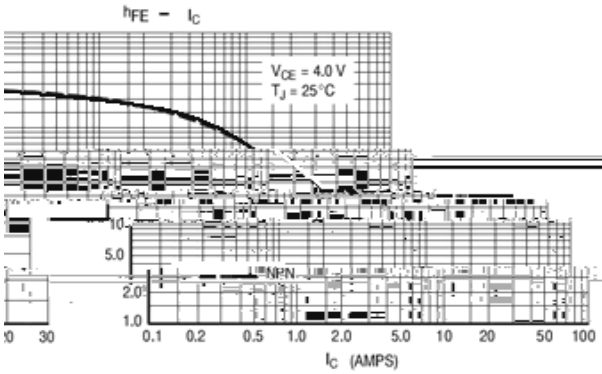
Symbol	Rating	Unit
V_{CBO}	100	V
V_{CEO}	100	V
V_{EBO}	5	V
I_C	25	A
I_{CM}	50	A
Total Power Dissipation	P_{tot}	125	W
Junction Temperature	T_j	150	℃
Storage Temperature Range	T_{stg}	-65 150	℃
Thermal Resistance Junction	R_{thj}	1	℃/W

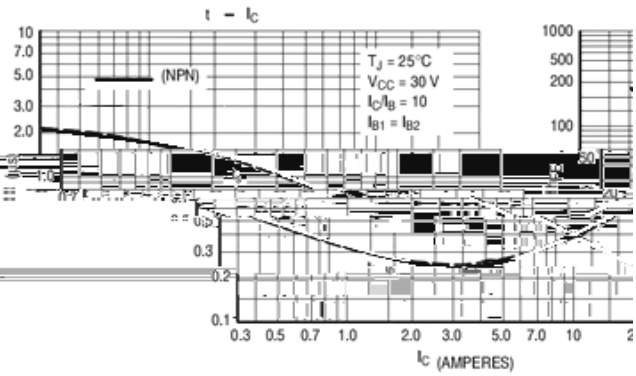
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector Cut-Off Current	I_{CEO}	V_{CE}=60V I_B=0			1	mA

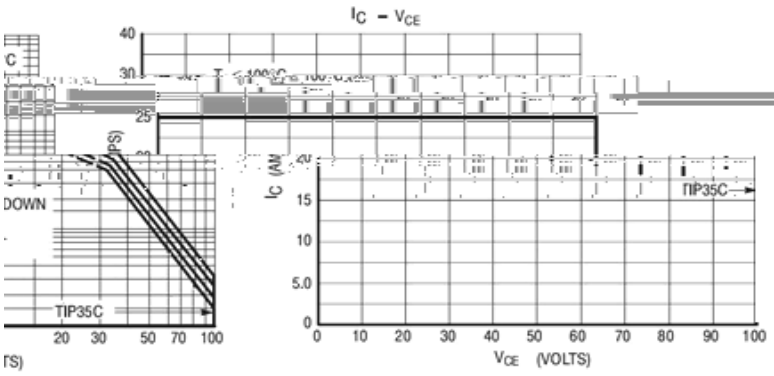
/ Electrical Characteristic Curve

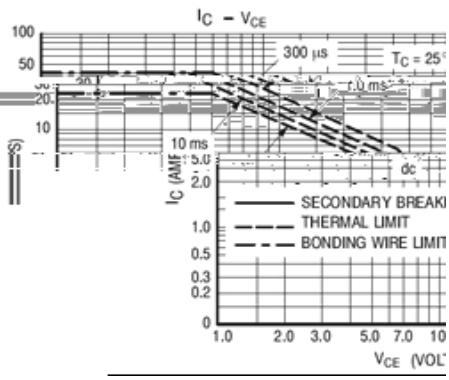




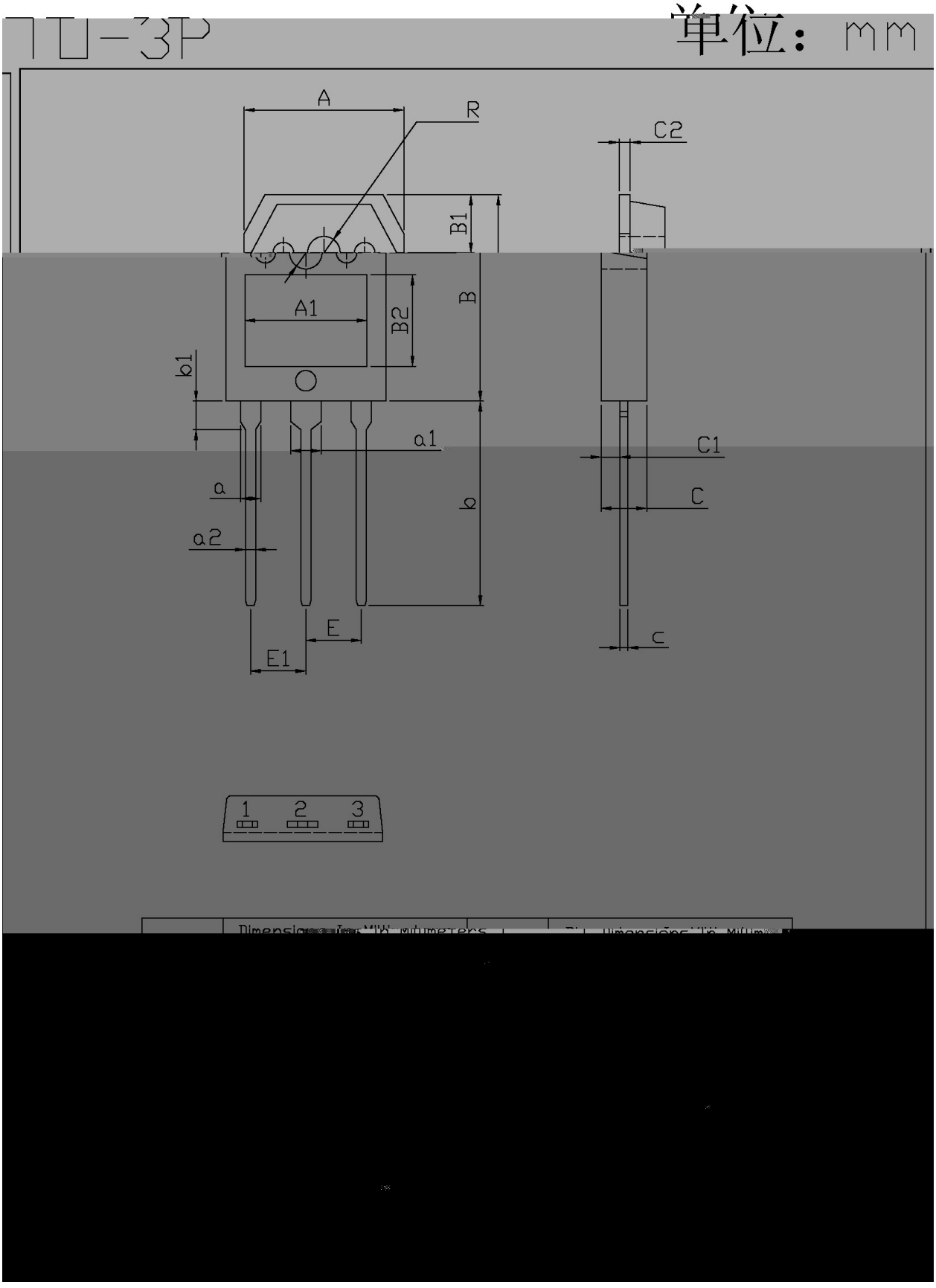




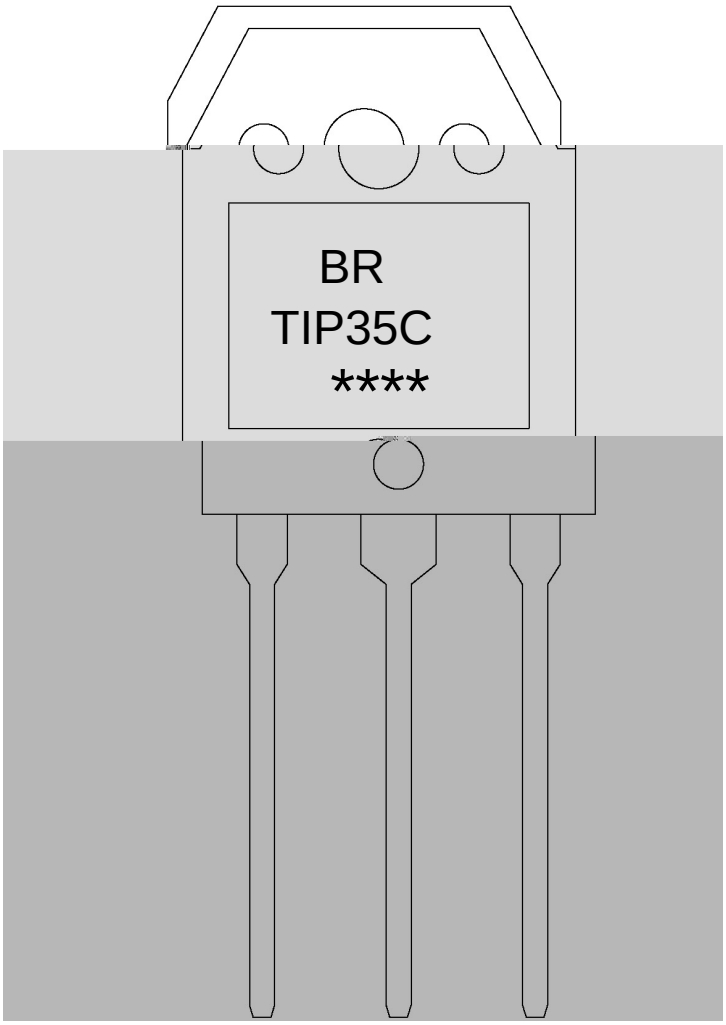




/ Package Dimensions



/ Marking Instructions



BR:

TIP35C

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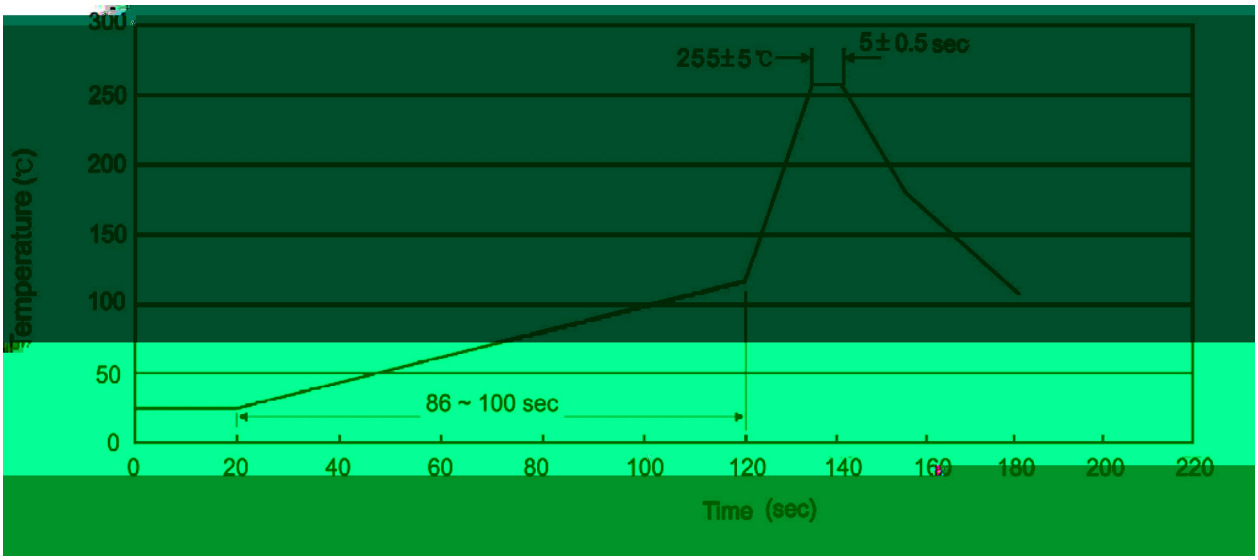
Note:

BR:	Company Code.
TIP35C:	Product Type.
****:	Lot No. Code, code change with Lot No.

( ) / Temperature Profile for Dip Soldering(Pb-Free)



Note:

1 25 150 60 90sec;

1.Preheating:25~150 , Time:60~90sec.

2 255±5 5±0.5sec;

2.Peak Temp.:255±5 , Duration:5±0.5sec.

3 2 10 /sec.

3. Cooling Speed: 2~10 /sec.

/ Resistance to Soldering Heat Test Conditions

270±5 10±1 sec. Temp.:270±5℃ Time:10±1 sec

/ Packaging SPEC.

/ TUBE

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm^3)		
	Units/Tube 只/套管	Tubes/Inner Box 套管/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Tube 套管	Inner Box 盒	Outer Box 箱
TO-3P	30	15	450	5	2250	497.5×46×8	555×164×50	575×290×180

/ Notices