

/ Descriptions

PDFN3×3A-8L P MOS

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/ Features

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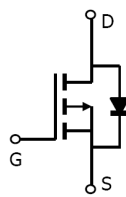
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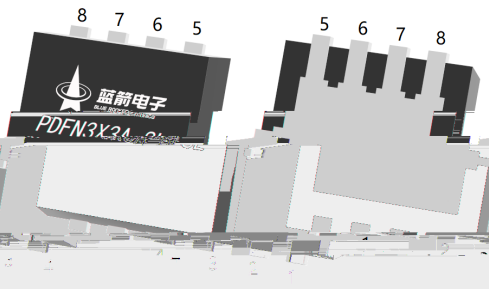
/ Applications

DC/DC

/ Equivalent Circuit



/ Pinning



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D

/ Marking

/ Absolute Maximum Ratings(Ta=25)

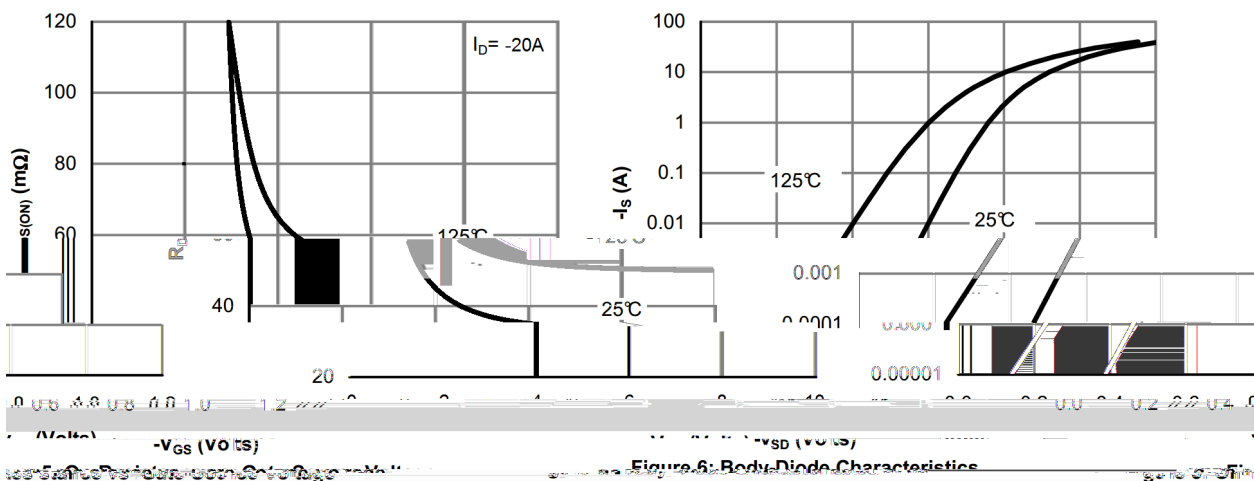
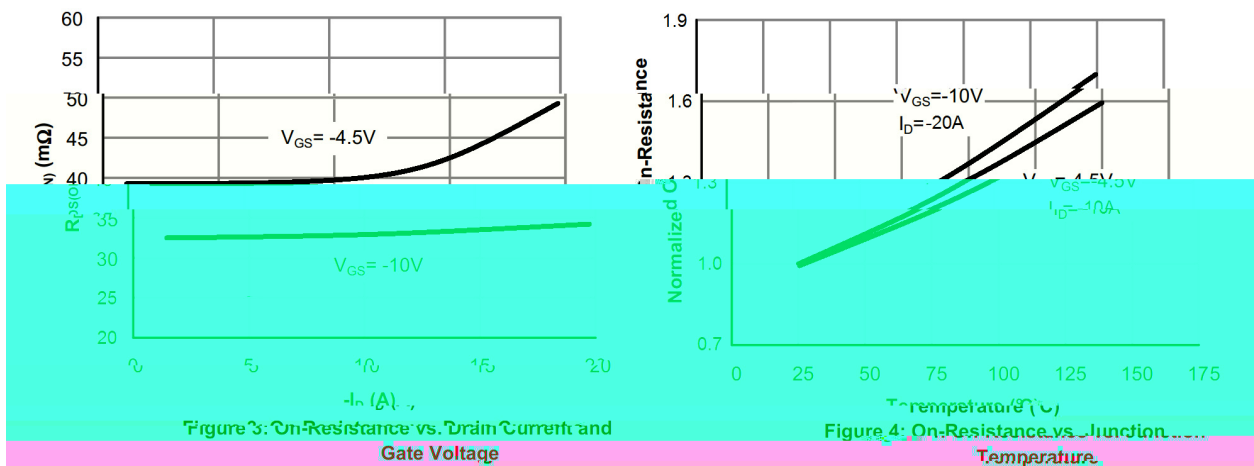
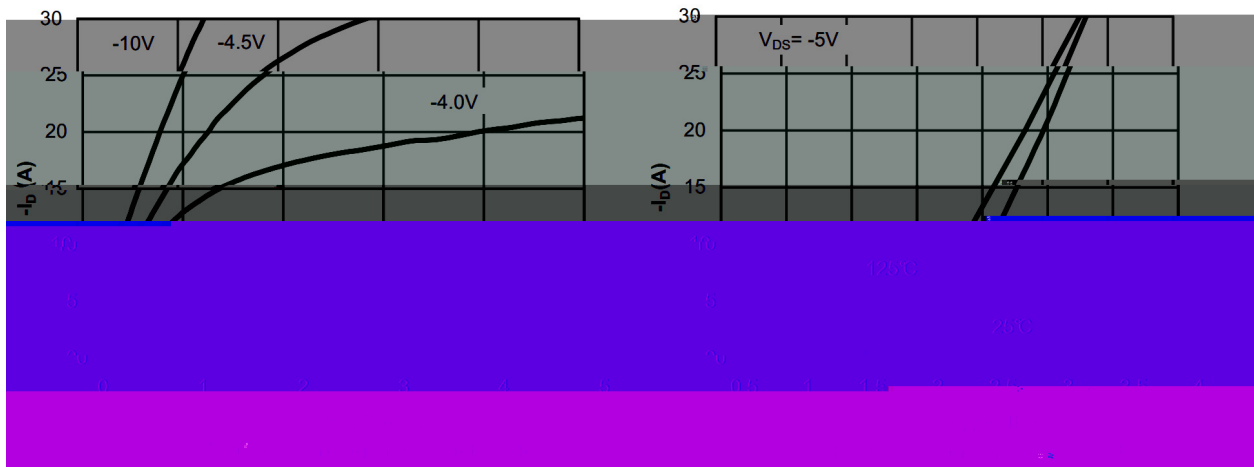
	≤	θ		
		θ		

/ Electrical Characteristics(Ta=25)

		μ				
						μ
						μ
		μ				
						Ω
						Ω

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/ Electrical Characteristic Curve



/ Electrical Characteristic Curve

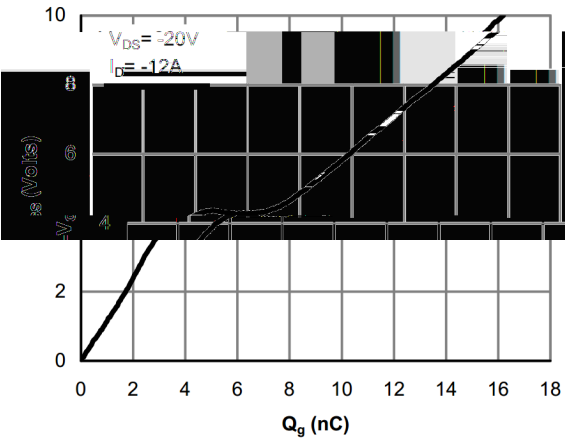


Figure 7: Gate-Charge Characteristics

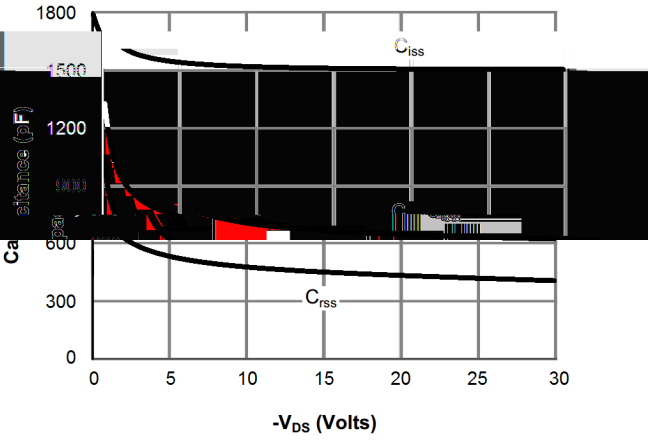
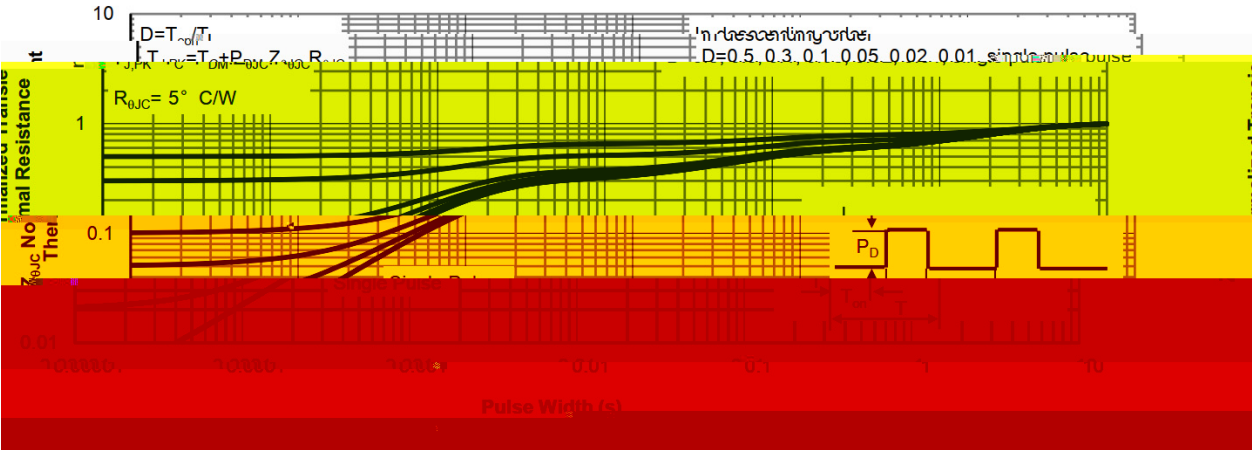
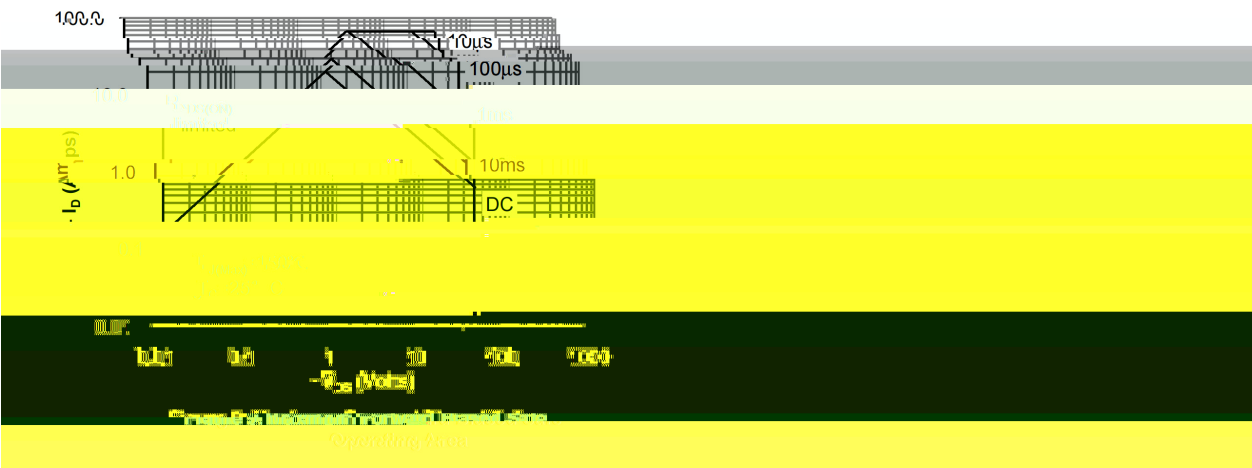
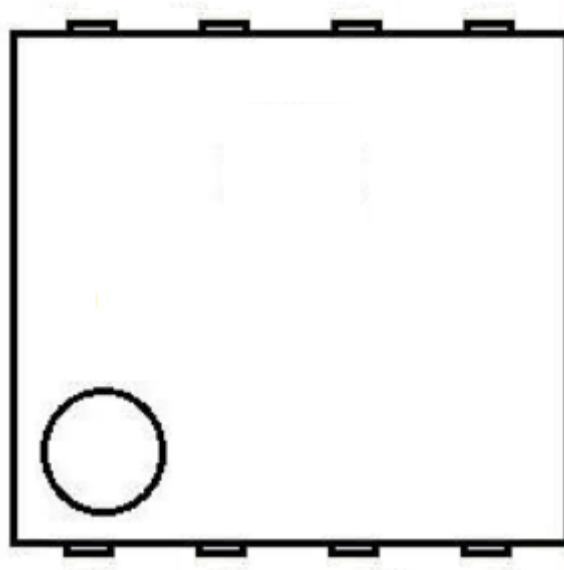


Figure 8: Capacitance Characteristics



/ Marking Instructions



() / Temperature Profile for IR Reflow Soldering(Pb-Free)

± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.