

PDFN3×3A-8L

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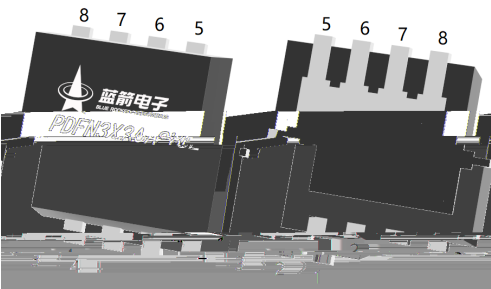
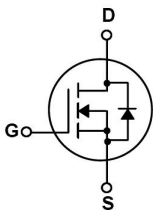
MOS

x

±

≤

DC/DC



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D



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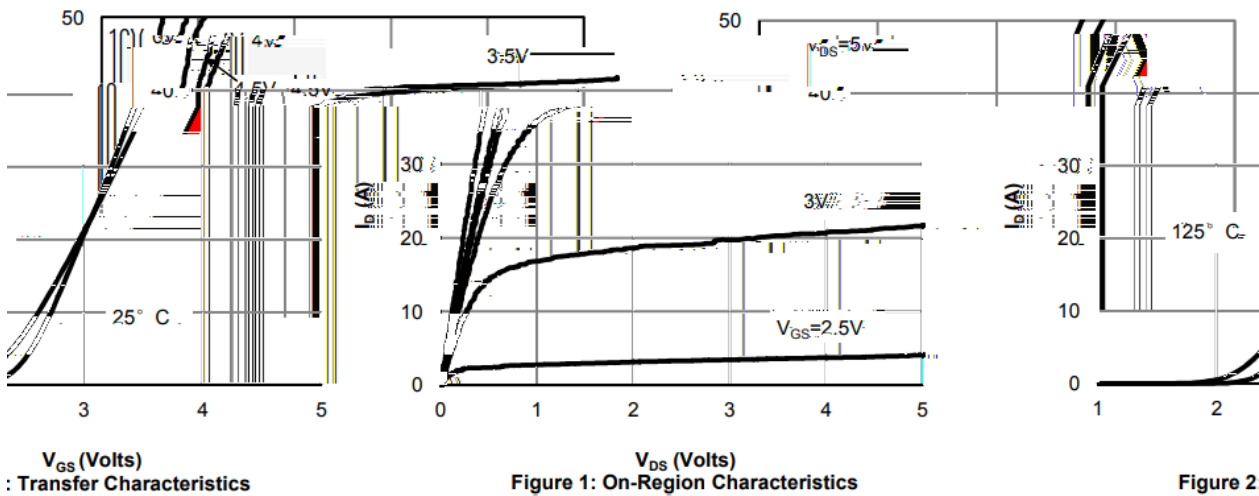


Figure 1: On-Region Characteristics

Figure 2

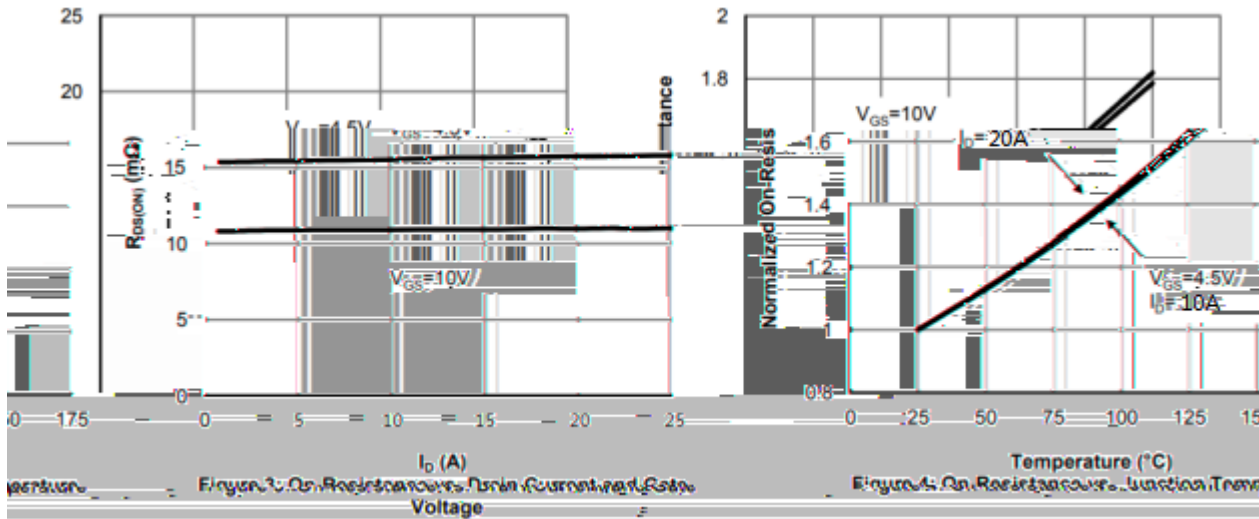


Figure 3: On-Resistance vs. Drain Current and Temperature

Figure 4: On-Resistance vs. Junction Temperature

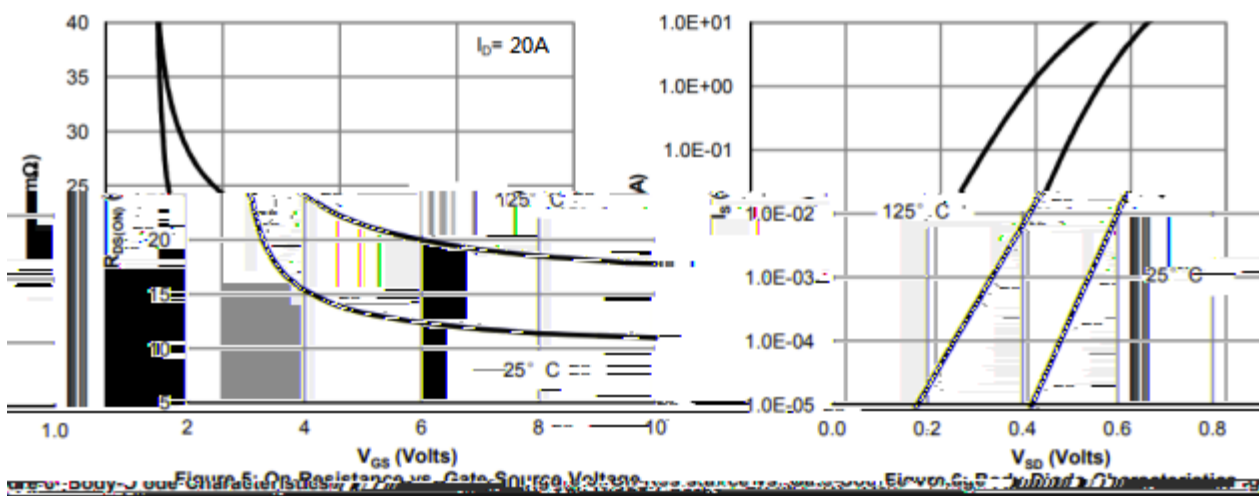


Figure 5: On-Resistance vs. Gate-Source Voltage

Figure 6: On-Resistance vs. Drain-Source Voltage

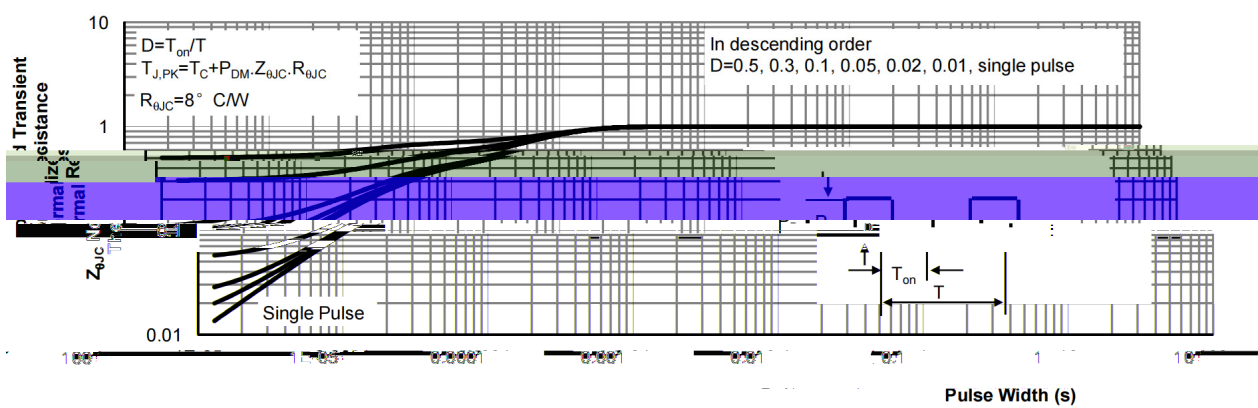
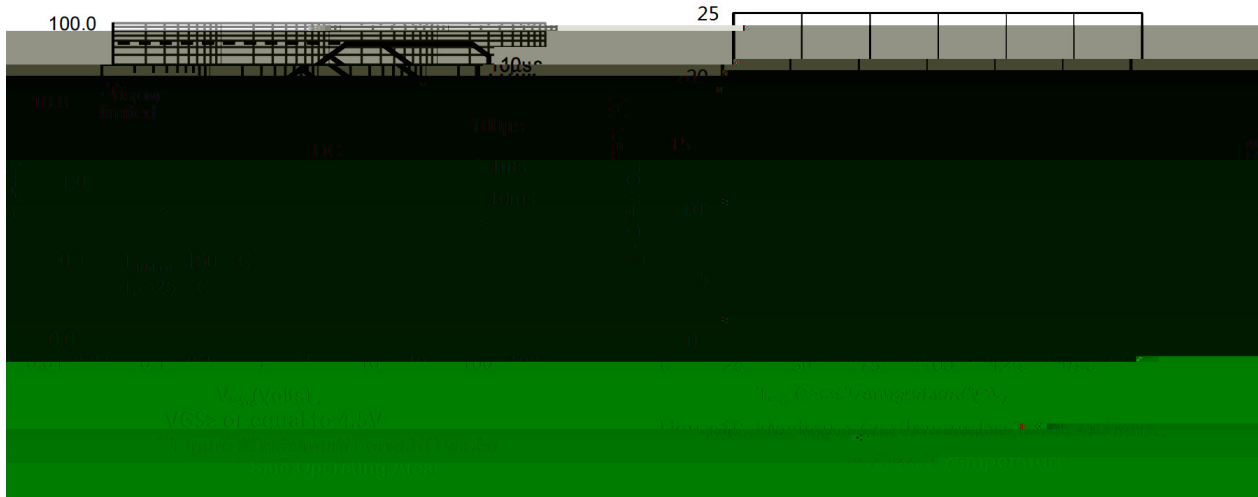
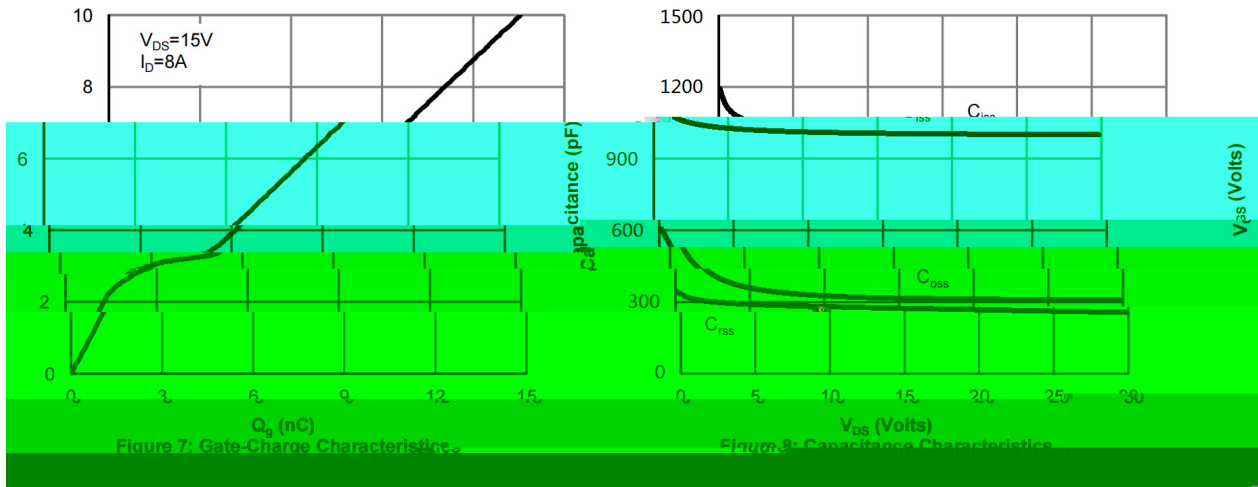
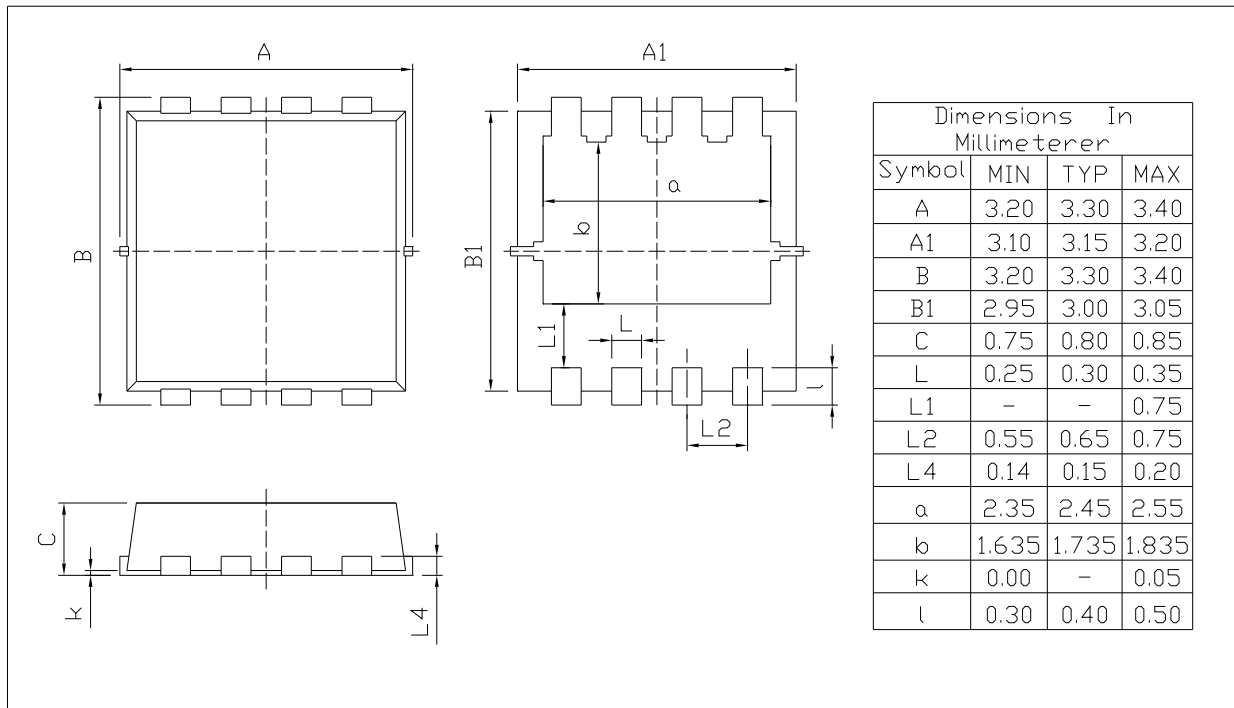


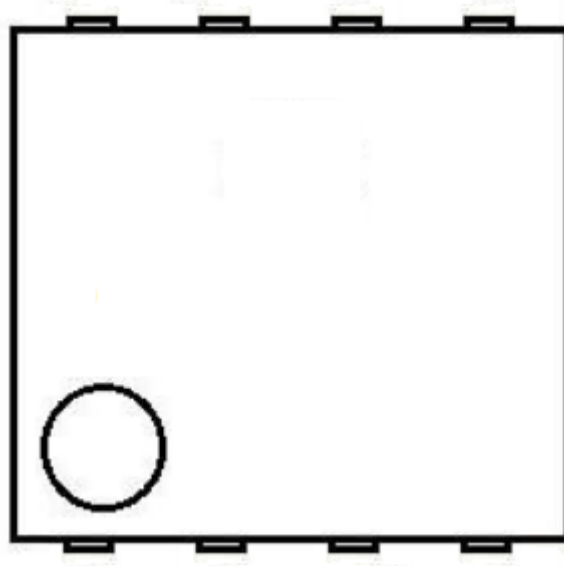
Figure 11: Normalized Maximum Transient Thermal Impedance

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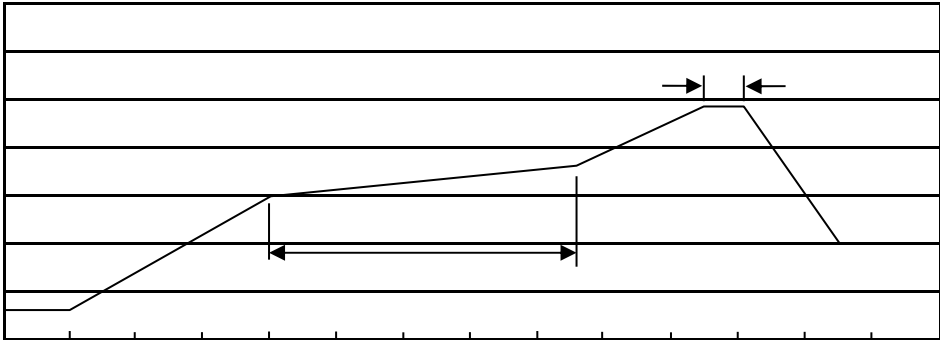
Unit:mm



Rev.00 202011



Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

± ±

7>û ~ E	;>û !H					;>û p . Æ		
	Units/Reel / --	Reels/Inner Box -- /-	Units/Inner Box /-	Inner Boxes/Outer Box - /1ç	Units/Outer Box /1ç	Reel	Inner Box -	Outer Box 1ç