

PDFN3×3A-8L

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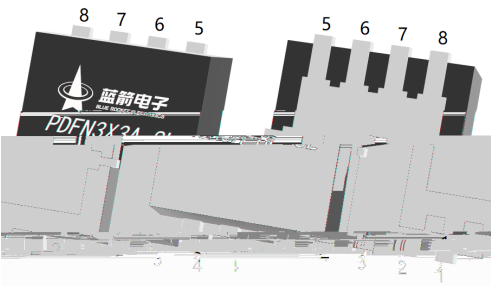
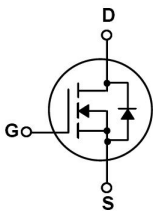
MOS

x

≤

≤

AEC-Q101



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D



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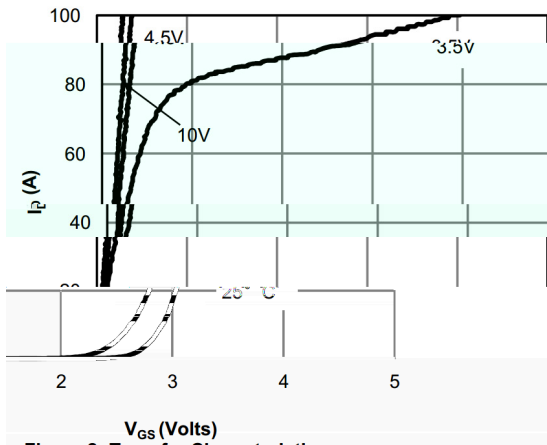


Figure 2: Transfer Characteristics

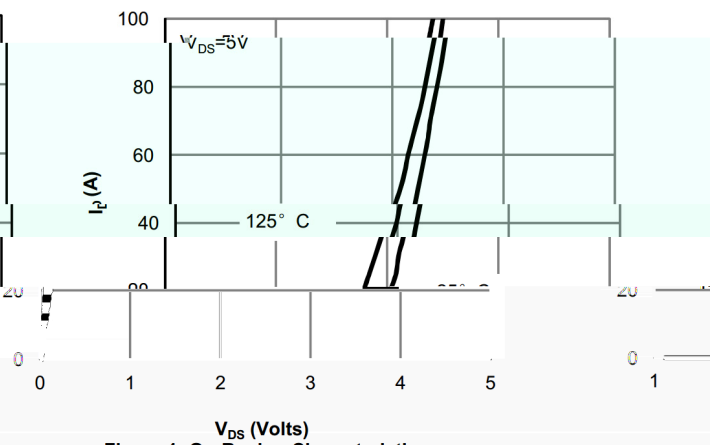


Figure 1: On-Region Characteristics

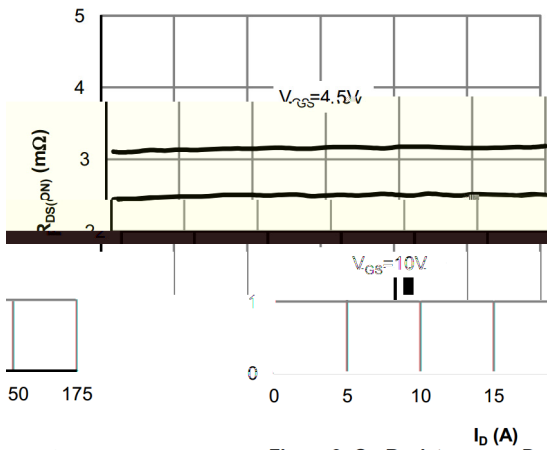


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

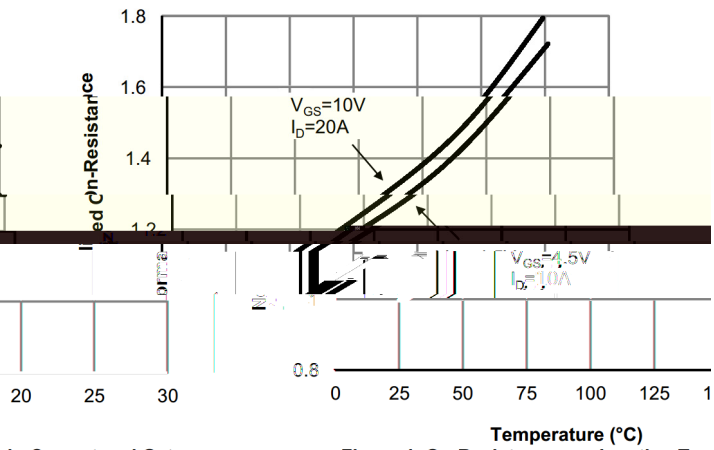


Figure 4: On-Resistance vs. Junction Temperature

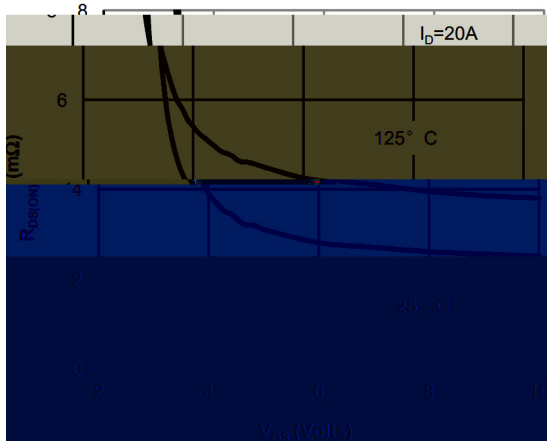


Figure 5: On-Resistance vs. Gate-Source Voltage

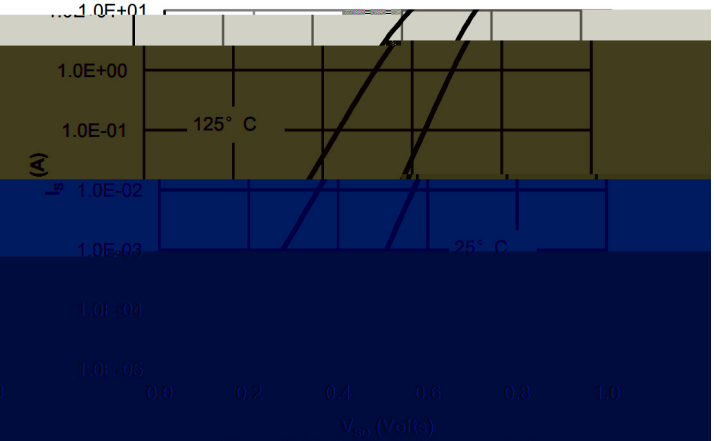


Figure 6: Body Diode Characteristics

I

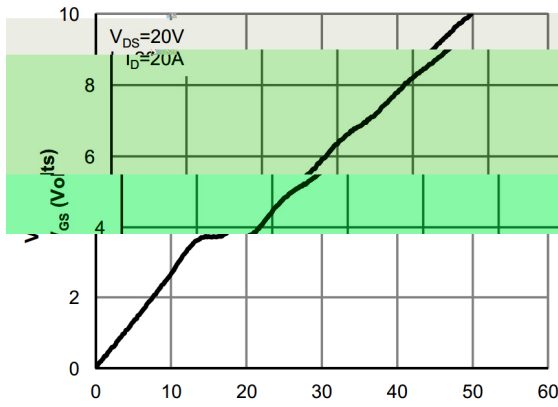


Figure 7: Gate-Charge Characteristics

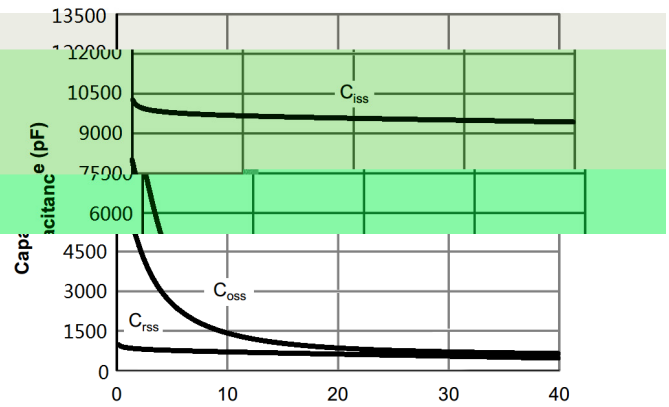
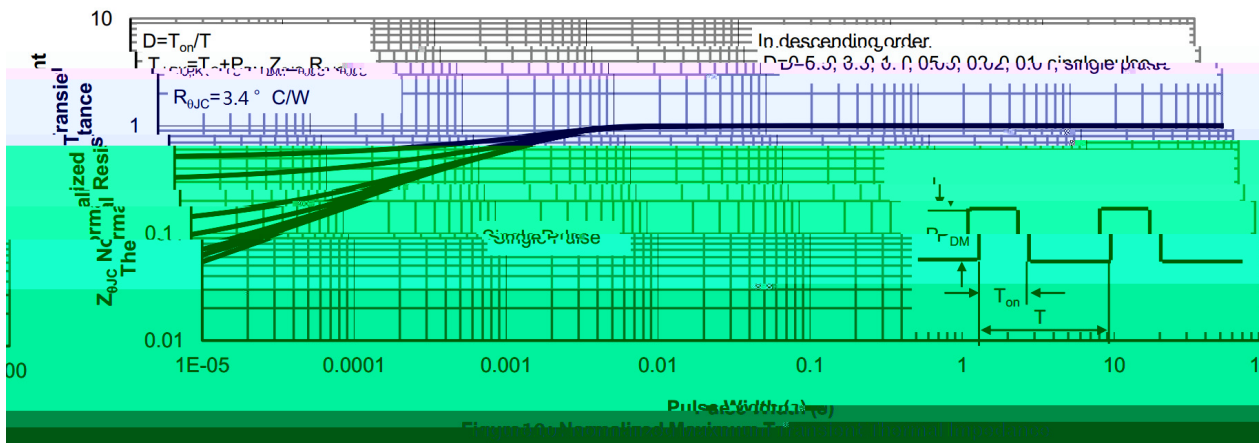
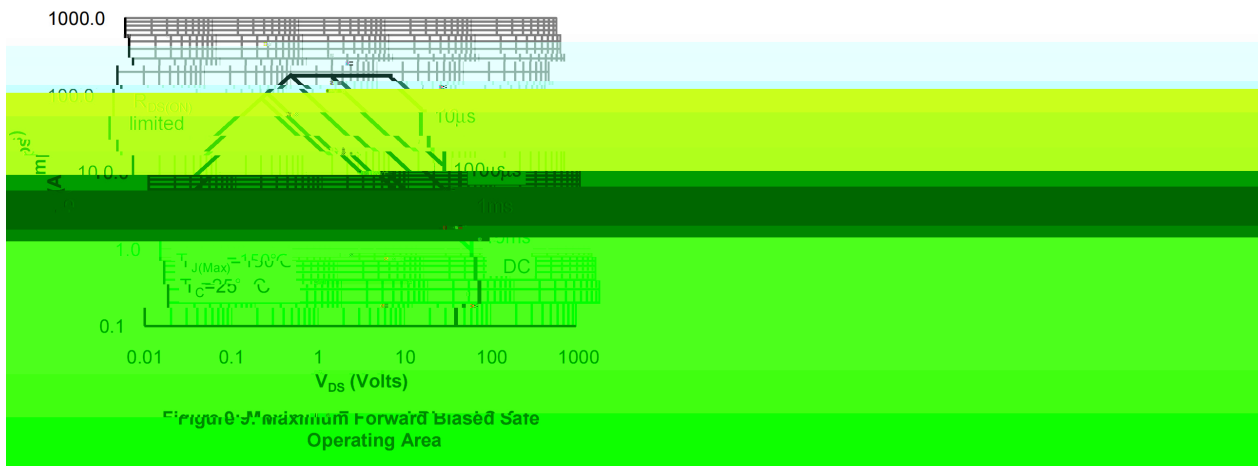
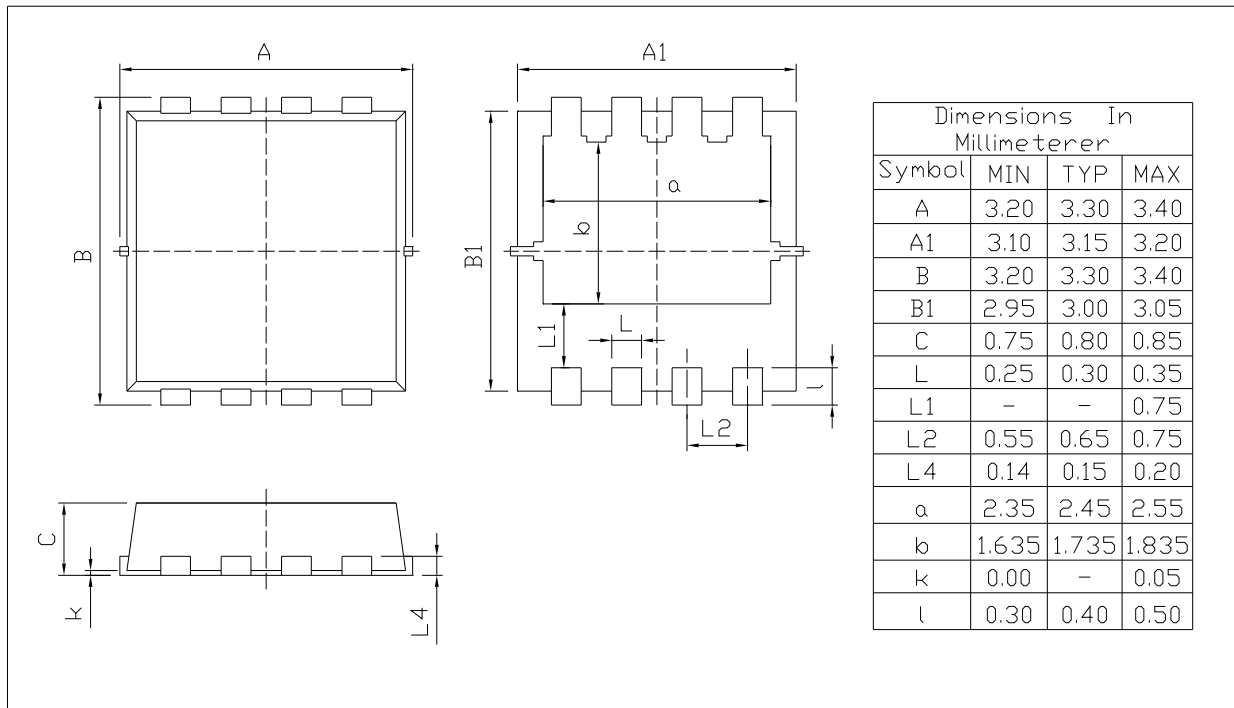


Figure 8: Capacitance Characteristics

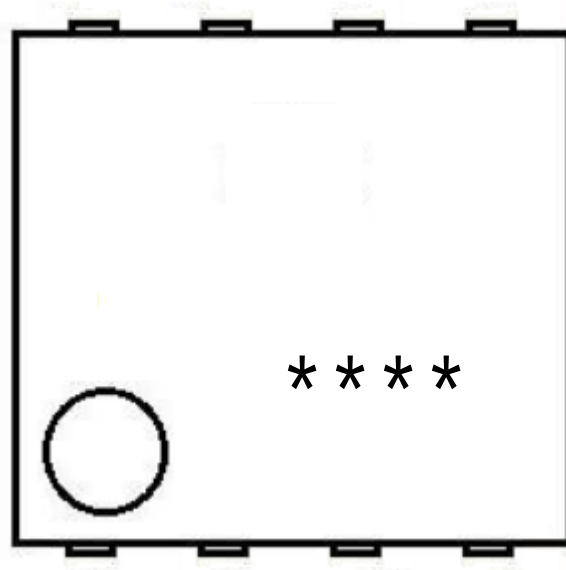


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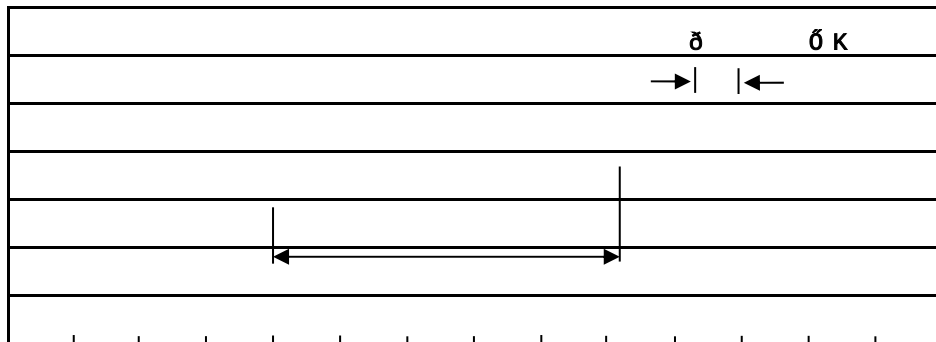
Unit:mm



Rev.00 202011



Temperature Profile for IR Reflow Soldering(Pb-Free)



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