

2SA940

Rev.H Oct.-2018

/ Descriptions

TO-220 PNP Silicon PNP transistor in a TO-220 Plastic Package.

/ Features

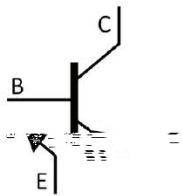
2SC2073

Complementary to 2SC2073.

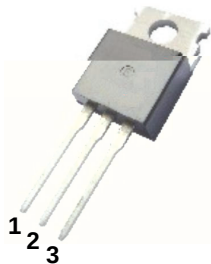
/ Applications

Power amplifier applications, vertical output applications.

/ Equivalent Circuit



/ Pinning



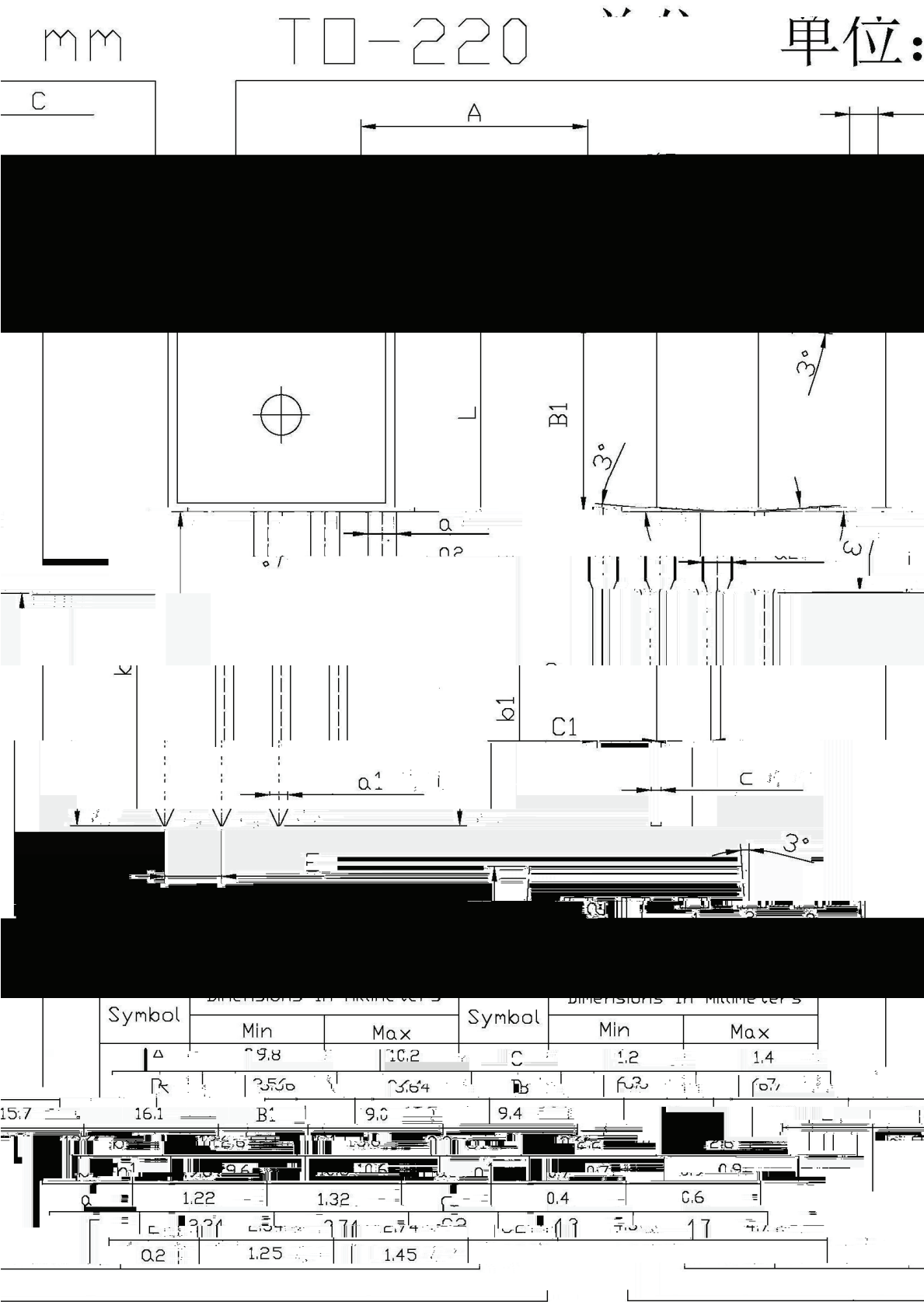
PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

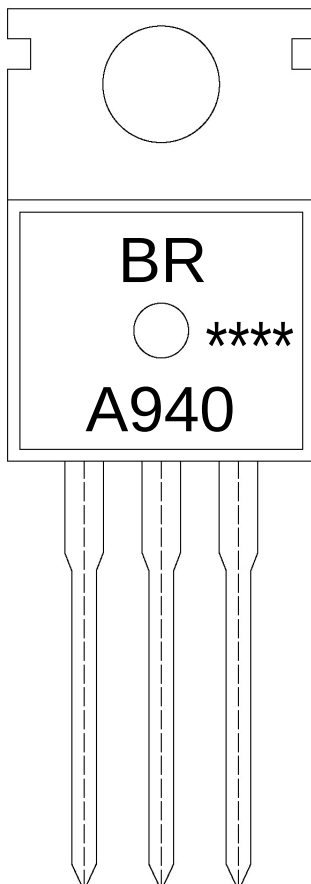
See Marking Instructions.

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-150	V
Collector to Emitter Voltage	V_{CEO}	-150	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-1.5	A
Base Current – Continuous	I_B	-0.5	A
Collector Power Dissipation	P_C	2.0	W
	$P_C(T_c=25^\circ C)$	25	W
Junction Temperature	T_j	150	

/ Package Dimensions



/ Marking Instructions



BR

A940

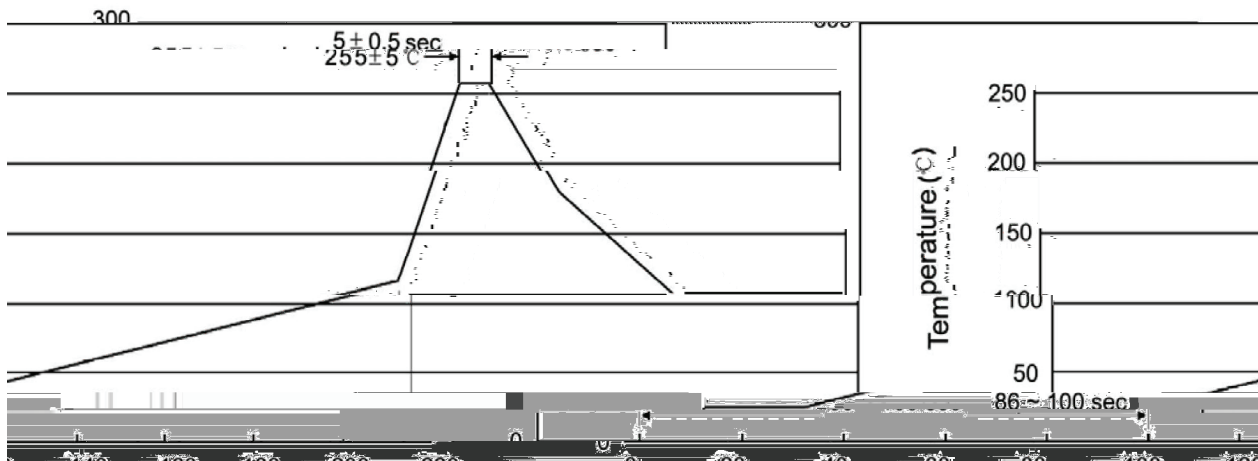
Note:

BR: Company Code

A940: Product Type.

****: Lot No. Code, code change with Lot No.

() / Temperature Profile for Dip Soldering(Pb-Free)



Note:

- | | | | | | |
|---|-------|-----|-----------|---|--------------------------------------|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 255±5 | | 5±0.5sec; | 2.Peak Temp.:255±5 , Duration:5±0.5sec. | |
| 3 | | 2 | 10 | /sec. | 3. Cooling Speed: 2~10 /sec. |

/ Resistance to Soldering Heat Test Conditions

270±5 10±1 sec. Temp.:270±5 Time:10±1 sec

/ Packaging SPEC.

/ BULK

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
	Units/Bag 只/袋	Bags/Inner Box 袋/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Bag 袋	Inner Box 盒	Outer Box 箱
TO-220/F	200	10	2,000	5	10,000	135×190	237×172×102	560×245×195

/ TUBE

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
	Units/Tube 只/套管	Tubes/Inner Box 套管/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Tube 套管	Inner Box 盒	Outer Box 箱
TO-220/F	50	20	1,000	5	5,000	532×31.4×5.5	555×164×50	575×290×180

/ Notices